

Appln No. 10/759,808  
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**Amendments to the Specification:**

Please replace paragraph number [0016] with the following amended paragraph:

[0016] FIG. 2 is a schematic diagram illustrating one exemplary embodiment of a reconfigurable detector included in the optical analysis system of FIG. 1 wherein m and n of the pixel array each equal eight.

Please replace paragraph number [0032] with the following amended paragraph:

[0032] FIG. 2 shows a detailed depiction of the detector 130 wherein m and n of the pixel array each equal eight in accordance with one embodiment of the invention. In this embodiment, the detector 130 includes one or more arrays of pixels 200, comprising a plurality of pixels, such as 201, 202, 204, 206, each of which has an associated programmable circuitry that is programmed with a user-defined function to carry out the designed analysis. The detector also comprises a pixel selector 210 that receives commands from a central processor (not shown) to select a specific group of arrayed pixels. A memory (not shown), such as a dynamically addressable random access memory, may be included in the detector to hold read-outs of the selected pixels for analysis.